

FIG. 3

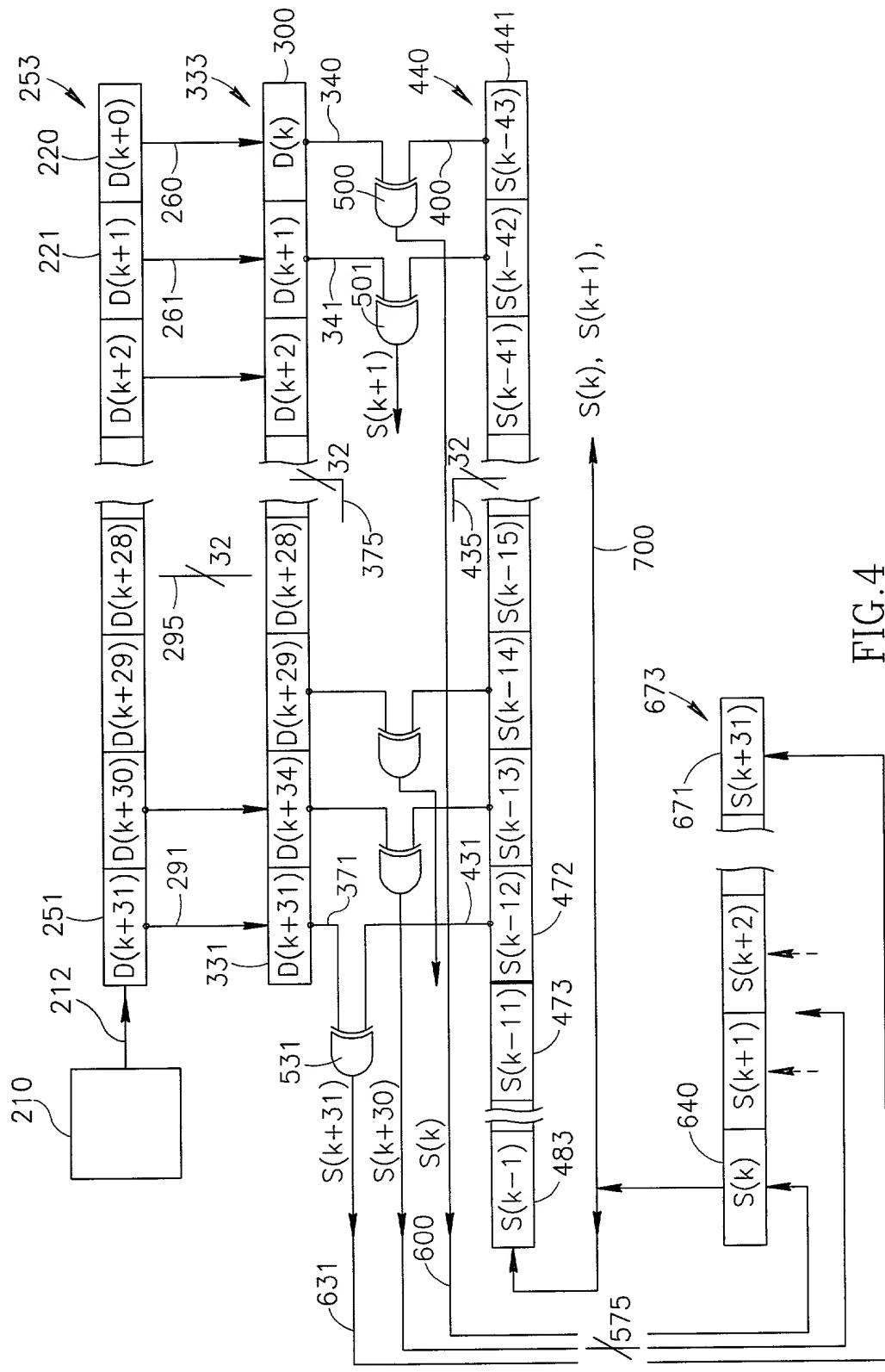
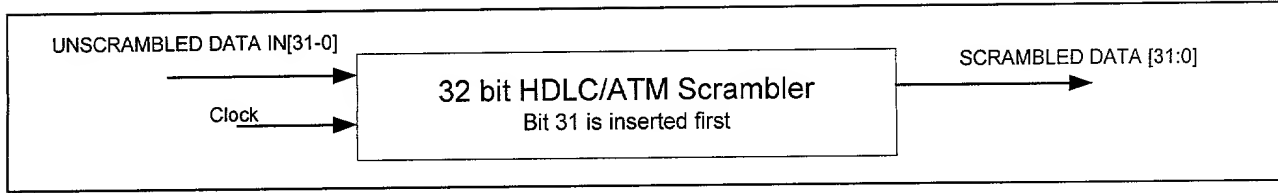


FIG. 4



FIG. 6



32 bit HDLC/ATM Scrambler Implementation

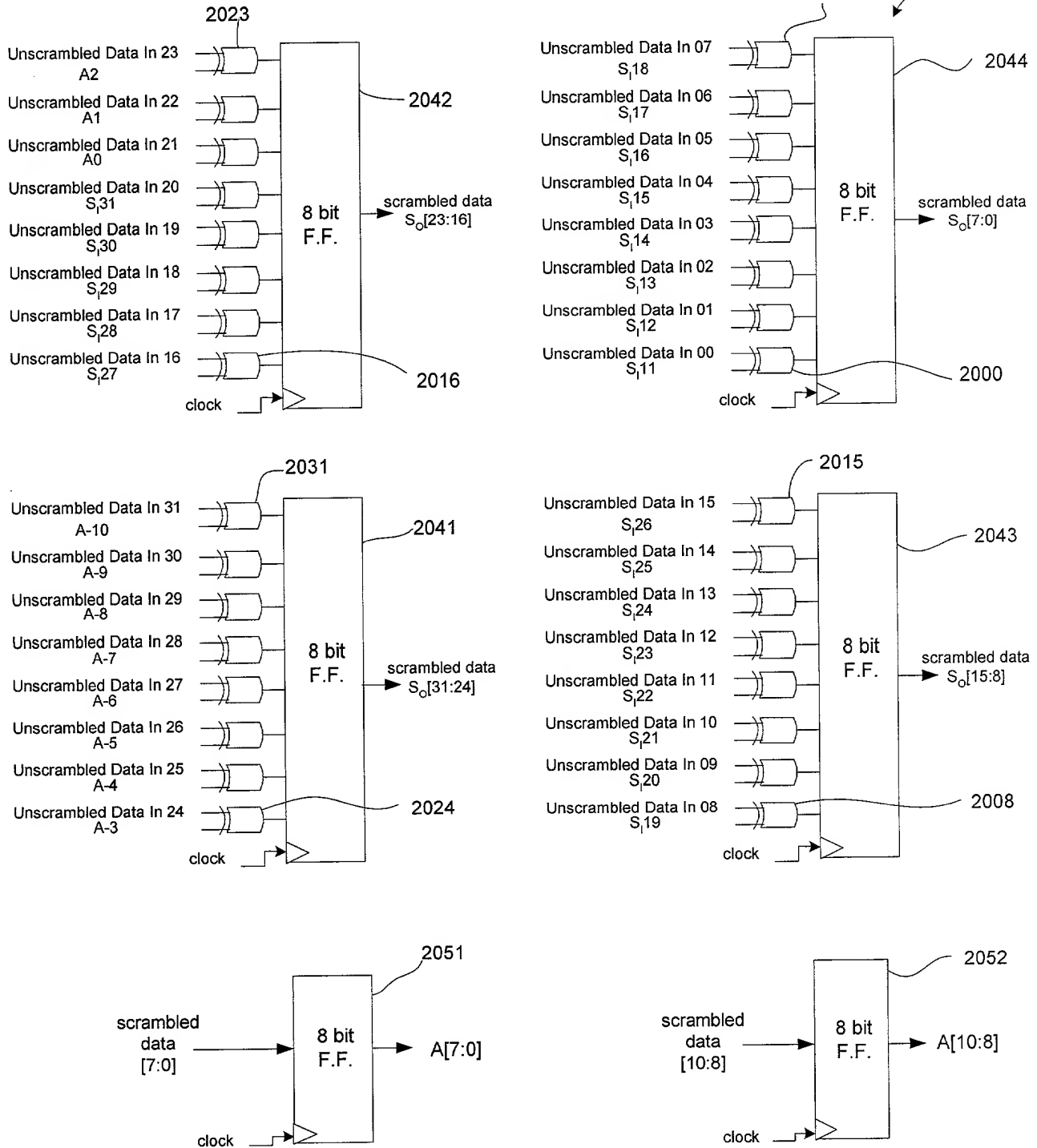
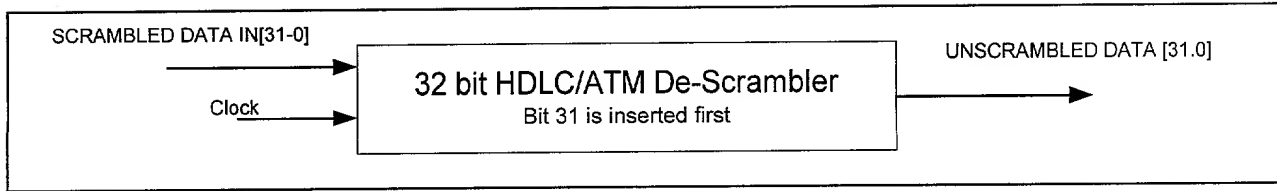


FIG. 7



32 bit HDLC/ATM De-Scrambler Implementation

